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10/734,003	12/10/2003	Choon-Yul Oh	50869/DBP/Y35	9397

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EXAMINER

NGUYEN, KIMNHUNG T

ART UNIT PAPER NUMBER

2629

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Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

This Application has been examined. The claims 1-22 are pending. The examination results are as following.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claim 17 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jinno (US 2002/0167472) in view of Chung et al. (US 2004/0051685)

As to claim 17, Jinno discloses in fig. 1, a display device comprising a display element for displaying a portion of an image in response to a current being applied; a transistor (Tr1) having a main electrode coupled to a voltage source (PVdd); a first capacitor (C1) for charging a first voltage Vsc); and a first switch, coupled between the transistor (Tr1) and the display for intercepting a current supplied to the display element from the transistor (Tr1).

However, Jinno does not disclose the first voltage corresponding to a threshold voltage of the transistor.

Chung et al. discloses in fig. 3, an active matrix organic light emitting diode comprising the voltage corresponding to a threshold voltage of the transistor (see abstract, see 0024).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the voltage corresponding to a threshold voltage of the transistor as taught by Chung et al. in to the display system of Jinno having the first voltage for producing the claimed invention because this would provide to the threshold voltage compensation circuit block, and then the video signal is input to the gate of the driving transistor of pixels. As a result, the threshold voltage nonuniformity between pixels can be reduced. Therefore, also, high-quality, large-sized displays can be implemented without increasing the area occupied by transistor in the pixels (see 0013).

Allowable Subject Matter

3. Claims 1-16 are allowed.

4. Claims 18-22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

3. The following is a statement of reasons for the indication of allowable subject matter:

None of the cited art teaches or suggests that a luminescent display comprising a second capacitor coupled between the power supply line and the second transistor for storing a voltages corresponding to the data signal; and a second switch for electrically isolating a second main electro of the first transistor from the luminescent element during voltage-charging of the first capacitor in response to a control signal, the first transistor supplying a current corresponding to the sum of the voltage charged in the first and second capacitors as claim 1; or a third transistor having a control electrode thereof coupled to a previous scan line for a pixel that was previously scanned, and coupled between the power supply line and the first and second capacitors; and a

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fourth transistor having a control electrode thereof coupled to the previous scan line, and being coupled between the second capacitor and the second main electrode of the first transistor, the first transistor supplying a current corresponding to a voltage charged in the first and second capacitors as claim 7; or applying a selection signal for selecting the pixel coupled to the scan line; and receiving the data voltage from the data line in response to the selection signal, and supplying a current corresponding to the sum of the compensated gate voltage and the data voltage to the luminescent element as claim 13; or wherein a first voltage is charged in the first capacitor during a first period, and a second voltage is charged in a second capacitor during a second period as claim 18.


Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number is (571) 272-7698. The examiner can normally be reached on MON-FRI, FROM 8:30 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on (571) 272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Kimnhung Nguyen
Patent Examiner
September 13, 2006